



Gp 2811

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Karl M. Robinson

Title: DEVICES HAVING IMPROVED CAPACITANCE AND METHODS OF THEIR FABRICATION

Docket No.: 303.455US3

Serial No.: 09/470,265

Filed: December 22, 1999

Due Date: April 4, 2001

Examiner: Cuong Q Nguyen

Group Art Unit: 2811

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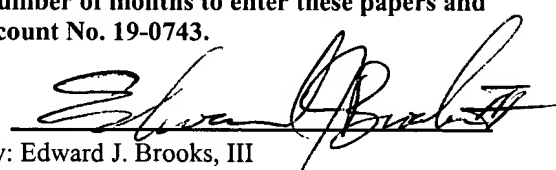
Commissioner for Patents  
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- X A return postcard.
- X An Amendment and Response (8 Pages).
- X A Clean Version of Pending Claims (6 pgs.).

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 30 day of March, 2001.

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S/N 09/470,265

PATENT

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**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**Commissioner for Patents  
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on January 4, 2001. Please amend the above-identified patent application as follows.

**IN THE CLAIMS**

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. Specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend the claims as follows:

19. (Amended) A capacitor, comprising:
  - a first conductive plate;
  - a second conductive plate; and
  - a dielectric interposed between said first and second conductive plates, wherein said dielectric is an oxide of a metal layer overlying [material of] the first conductive plate.
20. (Amended) A memory system, comprising:
  - a monolithic memory device, comprising a capacitor, wherein the capacitor comprises:
    - a first conductive plate;
    - a second conductive plate; and
    - a dielectric interposed between said first and second conductive plates, wherein said dielectric is an oxide of a metal layer overlying [material of] the first conductive plate; and
  - a processor configured to access the monolithic memory device.